# Tutorial Sheet
## 20<sup>th</sup> August, 2012 - 23<sup>rd</sup> August, 2012

1) Write an ARM Assembly Language program that will subtract two 64-bit integers stored in four registers.

2) How will you make an algorithm for signed multiplication?

3) How will you extend the traditional bit by bit algorithm for inputs that have a long run of 1s somewhere inside them?

4) What are the overflow conditions for addition and subtraction?