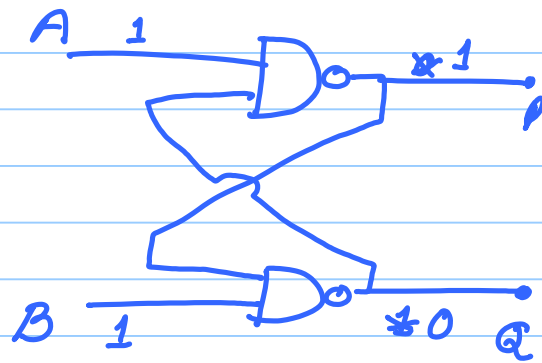
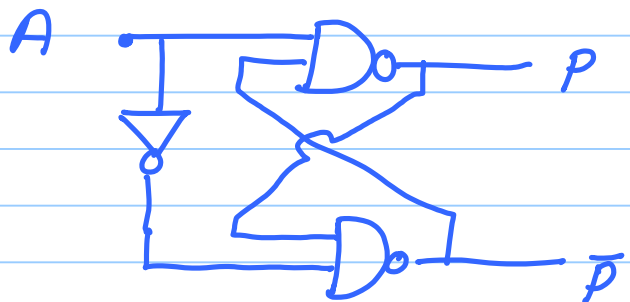


Sep-28-Ropar.



A	B	P	Q
1	0	0	1
0	1	1	0
x	0	1	1
x	1	1	1

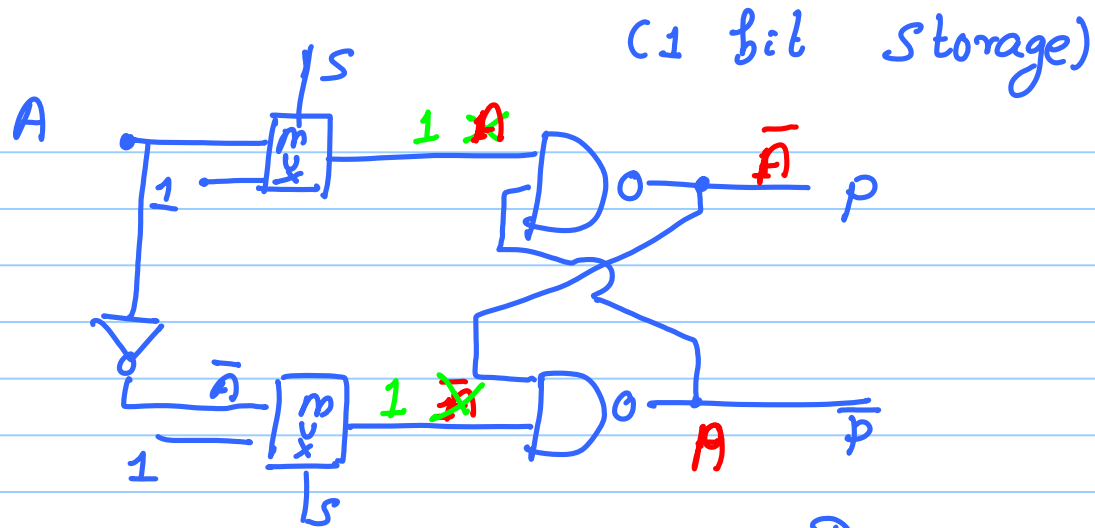
II



$$(A = \bar{P})$$

{ Cross-coupled fashion }

III



} Latch:
Level of the
signal.
→ Level Sensitive
latch.

$S = 1$

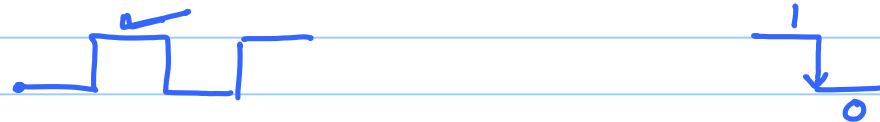
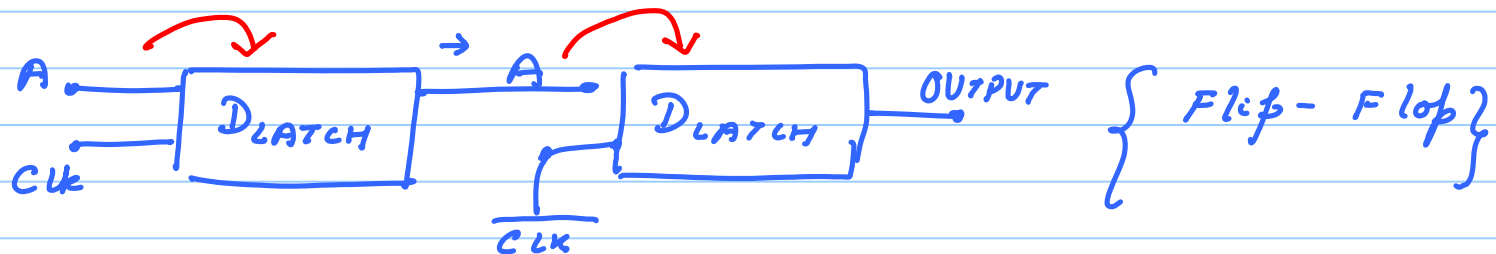
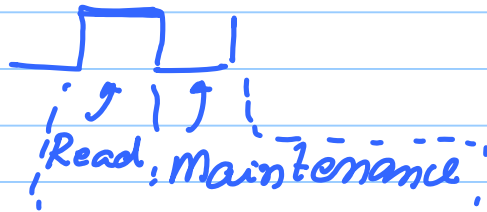
$[A = \bar{P}]$ D LATCH

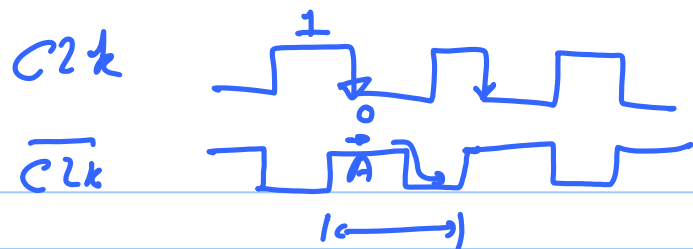
$S = 0$

Previous value is maintained.

{ This is the notion of memory }

Signal is typically the clock:

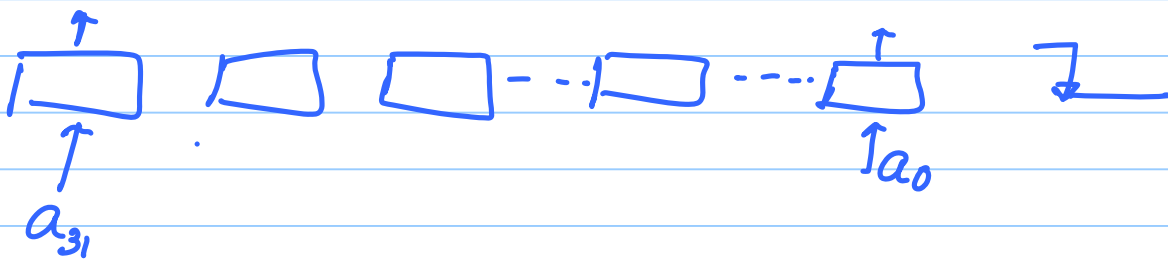




Edge triggered system



{ Register,
Latch



Summary.

1) Flip-flop: can save 1 bit

2) Register/Latch: 32 flip-flops.