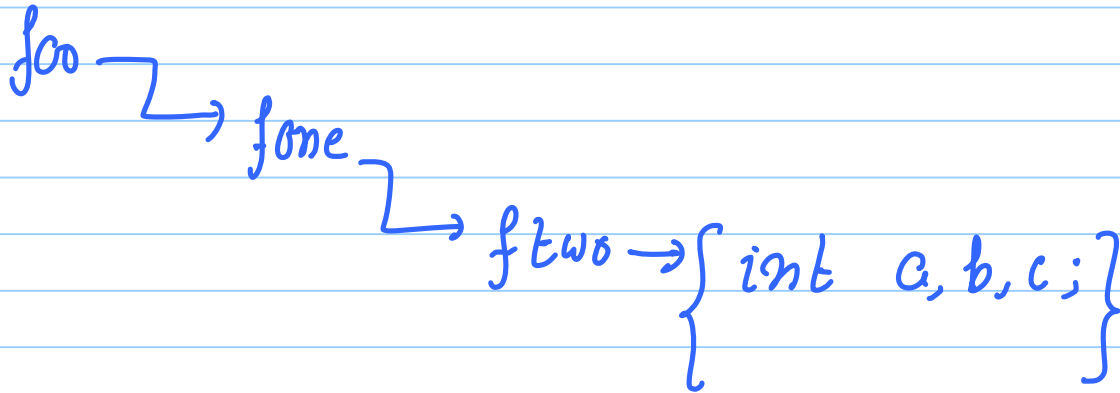


# Aug 1st



x Need more variables.

[sp]  
(stack pointer)

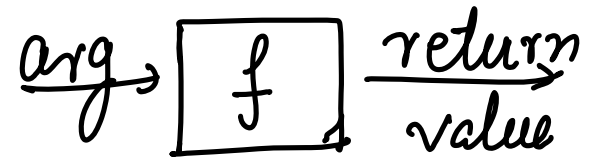


```

int ftwo() {
  int a, b, c;
  return (a + b - 10);
}

```

automatic vars.

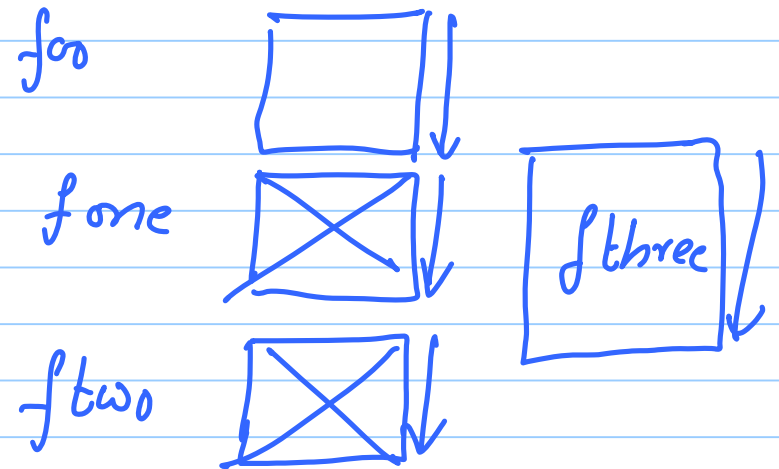


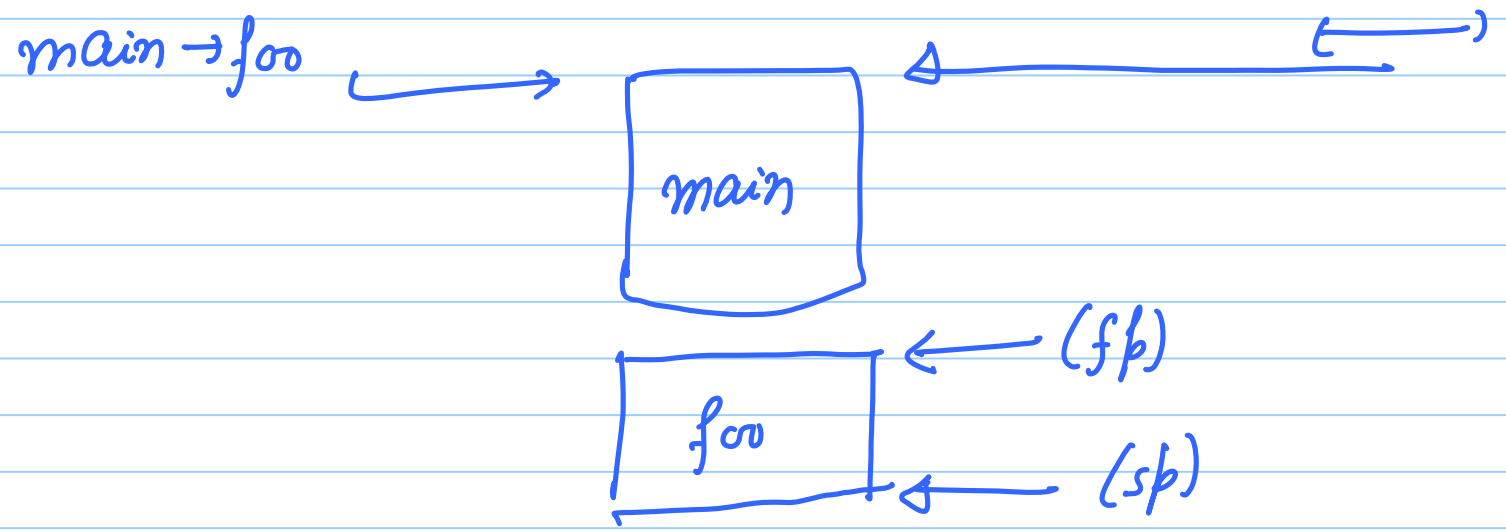
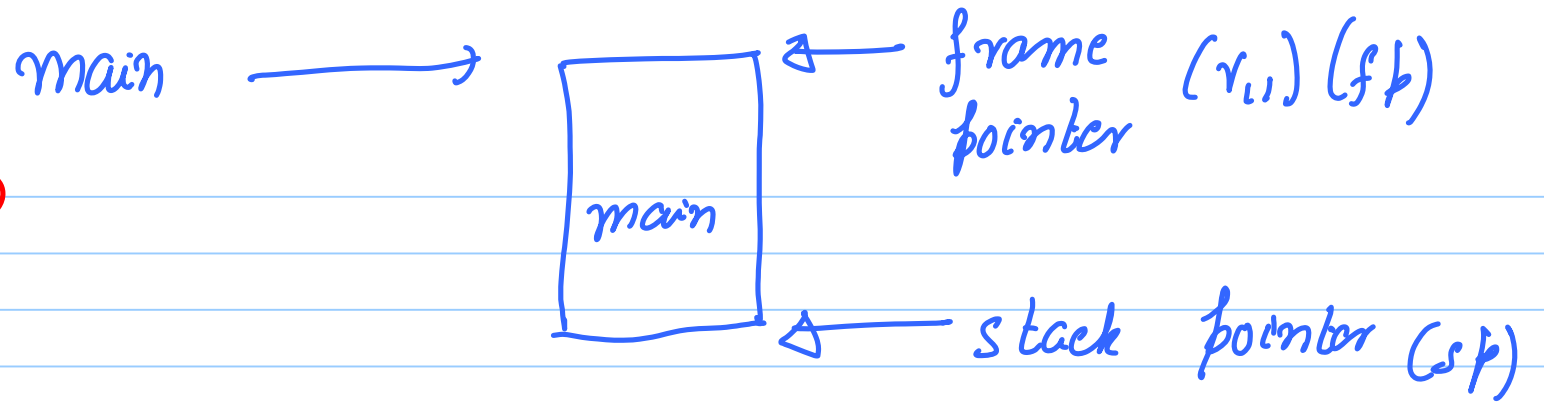
```

foo {
  fone();
  fthree();
}

fone {
  ftwo();
}

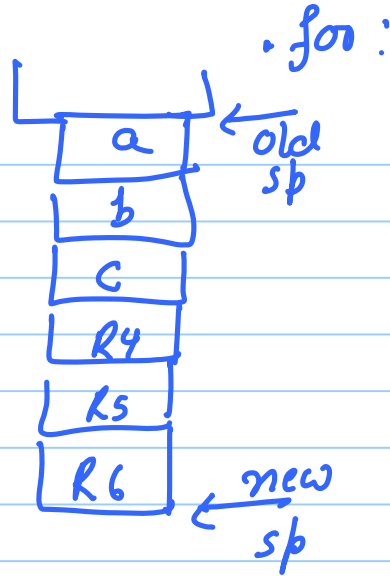
```





```
foo() {
  int a, b, c;
```

main (caller) → foo (callee)



(callee saved)

```
.foo:
  ADD sp, sp, #-24
  STR R6, [sp, #0]
  STR R5, [sp, #4]
  STR R4, [sp, #8]
  ;
```

} save

✓ more than 13 variables

✓ issue of recursion.

```
MOV R6, #10
```

```
LDR R6, [sp, #0]
LDR R5, [sp, #4]
```

} restore

reclaim  
space →

```
LDR R4, [sp, #8]
ADD sp, sp, #24 } restore sp
```

## Compilation Process

1) a.c

gcc a.c

./a.out

1) [www.gnuarm.com](http://www.gnuarm.com) ✓ (download & install)

arm-elf-gcc

arm-elf-run

2) palasi (details to be provided)

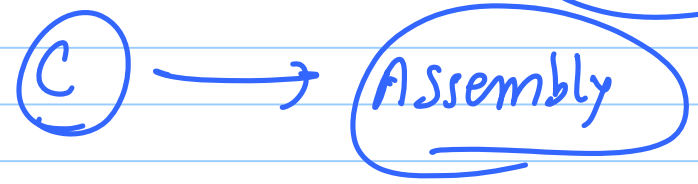
3) emuArm [end of this week or next week]

arm-elf-gcc -S ab.c

ab.s

[ course  
webpage ]

Assembly ?



Barring a few exceptions

1 assembly statement  $\longleftrightarrow$  1 machine instruction

Convert 1 assembly statement  
to  
sequence of 32 bits

Data proc instructions

format

Arithmetic + Logical. (R=00)

32

-10

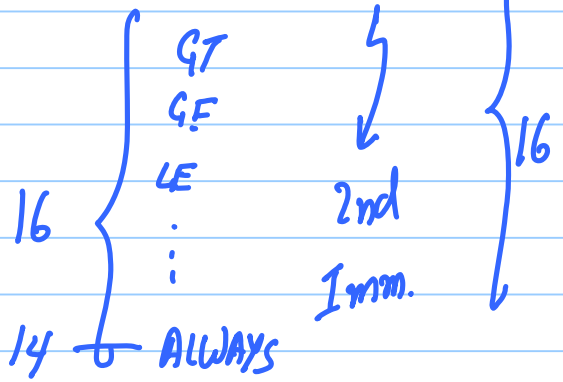
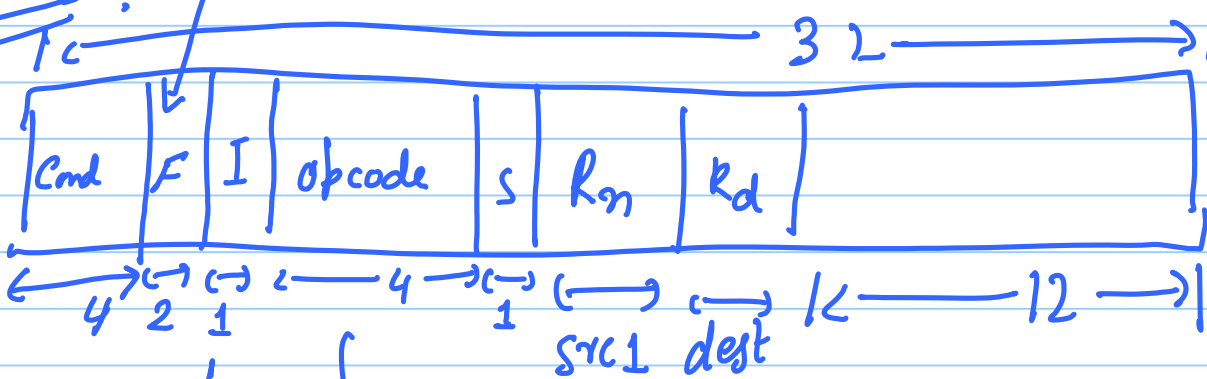
22

-8

14

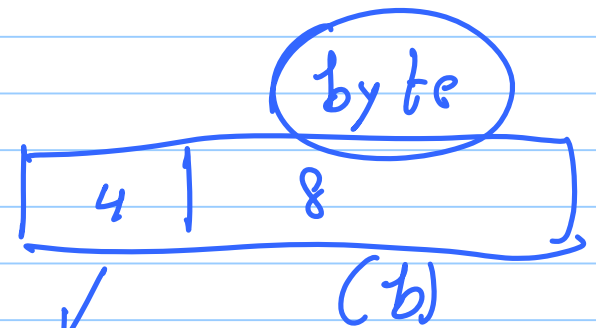
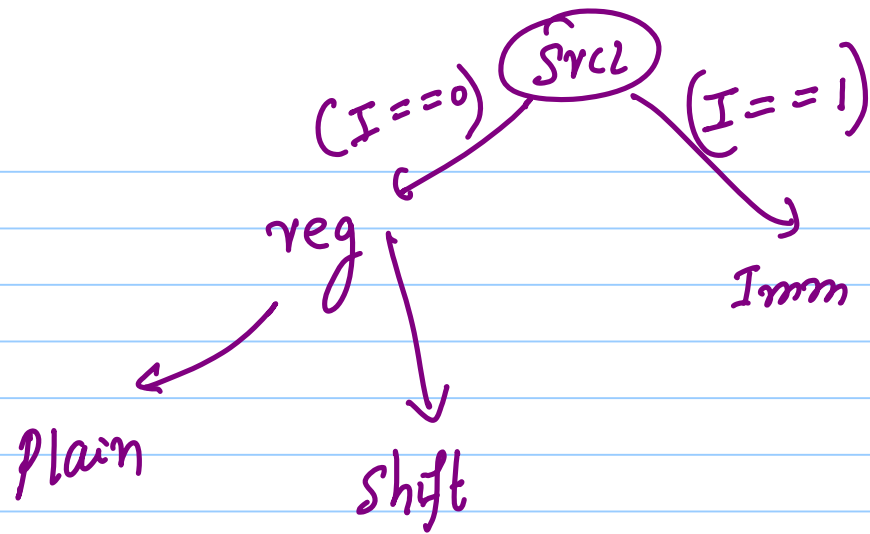
-2

12



(4) (4)

Immediate.



0-15

x2

0-30 (even)

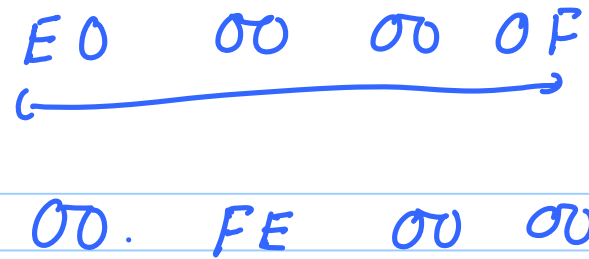
r

$$Imm = b \text{ ROR } r$$



E.g.

b = 0x FE



I<sub>1</sub> = 0x FE 12 39 A7

MOV R<sub>0</sub>, # A7

ORR R<sub>0</sub>, R<sub>0</sub>, # 3900

ORR R<sub>0</sub>, R<sub>0</sub>, # 12 00 00

ORR R<sub>0</sub>, R<sub>0</sub>, # FE 00 00 00

