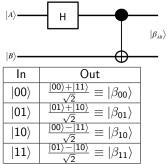
### COL863: Quantum Computation and Information

Ragesh Jaiswal, CSE, IIT Delhi

#### Introduction

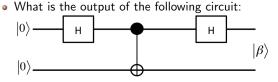
#### Some exercises:

• What is the output of the following circuit for different input states as shown:



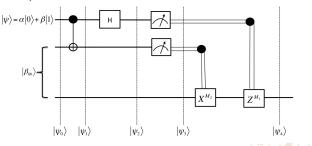
•  $|\beta_{00}\rangle$ ,  $|\beta_{01}\rangle$ ,  $|\beta_{10}\rangle$ ,  $|\beta_{11}\rangle$  are called Bell states or EPR-pairs or EPR-states (after Bell, Einstein, Podolsky, and Rosen). These exhibit interesting properties as we will see in our first application to quantum-teleportation.

#### Some exercises:

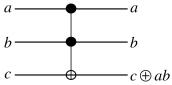


Ragesh Jaiswal, CSE, IIT Delhi COL863: Quantum Computation and Information

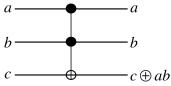
- Alice and Bob met sometime back and together they created Bell pair  $|\beta_{00}\rangle$  and both kept one qubit each.
- They are now very far from each other perhaps in some opposite corners of the universe.
- Alice wants to deliver an unknown qubit  $|\psi\rangle$  to Bob. Moreover, she can only communicate classical information to Bob.
- Fortunately, she knows quantum circuits and constructs the following circuit in a hope to communicate  $|\psi\rangle$ . The first two qubits in the circuit is in possession of Alice while Bob has the third qubit.



- Can we simulate classical logic circuit using a quantum circuit?
- <u>Claim</u>: Any classical logic circuit can be implemented using just NAND and COPY gates.
- If we can build a quantum analogue of NAND and COPY gates, then we will be done.
- The following three-qubit gate, called the Toffoli gate, can be used to implement both NAND and COPY.

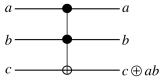


- Can we simulate classical logic circuit using a quantum circuit?
- <u>Claim</u>: Any classical logic circuit can be implemented using just NAND and COPY gates.
- If we can build a quantum analogue of NAND and COPY gates, then we will be done.
- The following three-qubit gate, called the Toffoli gate, can be used to implement both NAND and COPY.

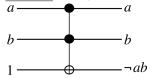


• Question: Can you build NAND using Toffoli gate?

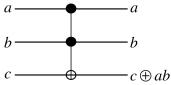
- Can we simulate classical logic circuit using a quantum circuit?
- <u>Claim</u>: Any classical logic circuit can be implemented using just NAND and COPY gates.
- If we can build a quantum analogue of NAND and COPY gates, then we will be done.
- The following three-qubit gate, called the Toffoli gate, can be used to implement both NAND and COPY.



• Question: Can you build NAND using Toffoli gate?

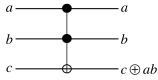


- Can we simulate classical logic circuit using a quantum circuit?
- <u>Claim</u>: Any classical logic circuit can be implemented using just NAND and COPY gates.
- If we can build a quantum analogue of NAND and COPY gates, then we will be done.
- The following three-qubit gate, called the Toffoli gate, can be used to implement both NAND and COPY.

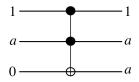


• Question: Can you build COPY using Toffoli gate?

- Can we simulate classical logic circuit using a quantum circuit?
- <u>Claim</u>: Any classical logic circuit can be implemented using just NAND and COPY gates.
- If we can build a quantum analogue of NAND and COPY gates, then we will be done.
- The following three-qubit gate, called the Toffoli gate, can be used to implement both NAND and COPY.

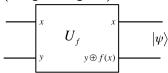


• Question: Can you build COPY using Toffoli gate?

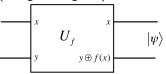


- Can we simulate classical logic circuit using a quantum circuit? Yes
- Can quantum circuits do more than just simulating classical ones?
  - We will introduce the idea of quantum parallelism. The main idea is simultaneous evaluation of a function over various inputs.
  - We will look at Deutsch's Algorithm which is a prototypical example used to demonstrate the idea of quantum parallelism.

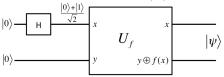
- Consider any boolean function over one-bit inputs  $f: \{0,1\} \rightarrow \{0,1\}.$
- <u>Claim</u>: It is possible to construct the following quantum gate  $U_f$  (using basic gates):



- Consider any boolean function over one-bit inputs  $f: \{0,1\} \rightarrow \{0,1\}.$
- <u>Claim</u>: It is possible to construct the following quantum gate  $U_f$  (using basic gates):

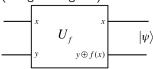


- By feeding inputs  $|00\rangle$  and  $|10\rangle$ , we can compute f(0) and f(1).
- What happens when we feed the input  $|+\rangle |0\rangle$  in this circuit? What is the output state  $|\psi\rangle$ ?

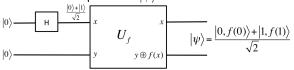


# $\begin{array}{l} \textbf{Introduction} \\ \textbf{Quantum algorithms} \rightarrow \textbf{Deutsch's algorithm} \end{array}$

- Consider any boolean function over one-bit inputs  $f: \{0,1\} \rightarrow \{0,1\}.$
- <u>Claim</u>: It is possible to construct the following quantum gate  $U_f$  (using basic gates):



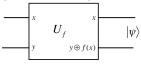
- By feeding inputs  $|00\rangle$  and  $|10\rangle$ , we can compute f(0) and f(1).
- What happens when we feed the input  $|\beta_{00}\rangle |0\rangle$  in this circuit? What is the output state  $|\psi\rangle$ ?



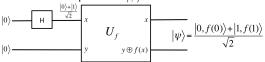
• This output state contains simultaneous evaluations of both f(0) and f(1)!

# $\begin{array}{l} \textbf{Introduction} \\ \textbf{Quantum algorithms} \rightarrow \textbf{Deutsch's algorithm} \end{array}$

- Consider any boolean function over one-bit inputs  $f : \{0, 1\} \rightarrow \{0, 1\}.$
- <u>Claim</u>: It is possible to construct the following quantum gate  $U_f$  (using basic gates):



- By feeding inputs  $|00\rangle$  and  $|10\rangle$ , we can compute f(0) and f(1).
- What happens when we feed the input  $|\beta_{00}\rangle |0\rangle$  in this circuit? What is the output state  $|\psi\rangle$ ?

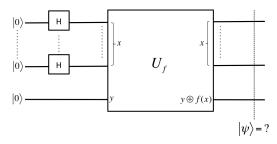


- This output state contains simultaneous evaluations of both f(0) and f(1)!
- <u>Question</u>: Can we generalize this idea for boolean functions over multiple bit inputs?

- <u>Question</u>: Can we generalize this idea for boolean functions over multiple bit inputs?
- Consider any boolean function over *n*-bit inputs

$$f: \{0,1\}^n \to \{0,1\}.$$

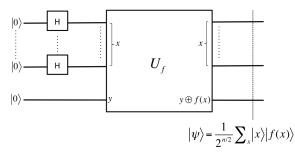
• What is the output of the following circuit?



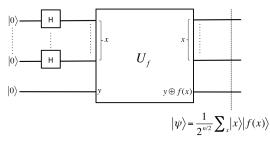
- <u>Question</u>: Can we generalize this idea for boolean functions over multiple bit inputs?
- Consider any boolean function over *n*-bit inputs

$$f: \{0,1\}^n \to \{0,1\}.$$

• What is the output of the following circuit?



- <u>Question</u>: Can we generalize this idea for boolean functions over multiple bit inputs?
- Consider any boolean function over *n*-bit inputs  $f: \{0,1\}^n \to \{0,1\}.$
- What is the output of the following circuit?



• Even though final state encodes evaluation of the function on all inputs, what we can measure is only one of them. So, it is important that we do not get carried away by the potential quantum parallelism exhibited in the above circuit.

### End

Ragesh Jaiswal, CSE, IIT Delhi COL863: Quantum Computation and Information