

Dilpreet Kaur

PERSONAL DETAILS

Birth September 12, 1990
Address I-304, Govindpuram, Ghaziabad, India
Mail dilpreetdan@gmail.com

EDUCATION

PhD. Computer Science and Engineering 2015 - present

Indian Institute of Technology, Delhi

Area: Approximation Algorithms for Facility Location Problems

Advisor: Prof. Naveen Garg

M.Tech. Computer Science and Automation 2011-2013

Indian Institute of Science, Bangalore

Advisor: Prof. Y. Narahari

CGPA: 6.6/8.0 with A grade in project

B.Tech. Computer Science and Engineering 2007-2011

JSS Academy of Technical Education, Noida

Percentage: 80.15%

WORK EXPERIENCE

Research Engineer 2013-2015

Center for Development of Telematics, New Delhi, Full-time

Deployed OpenStack for private cloud services. Developed soft phone using SIP protocol.

PUBLICATIONS

Incentive Compatible Mechanisms for Power Cut Allocation in Smart Grids

9th IEEE International Conference on Automation Science and Engineering

In this paper, we propose (i) a reverse auction mechanism and (ii) a forward auction mechanism that both are designed to allocate the power cuts. These auctions are based on redistribution mechanisms and satisfy dominant strategy incentive compatibility and allocation efficiency. The mechanisms are weakly budget balanced and since they are based on redistribution mechanisms, they lead to reduced budget imbalance.

REFERENCES

Available on request.