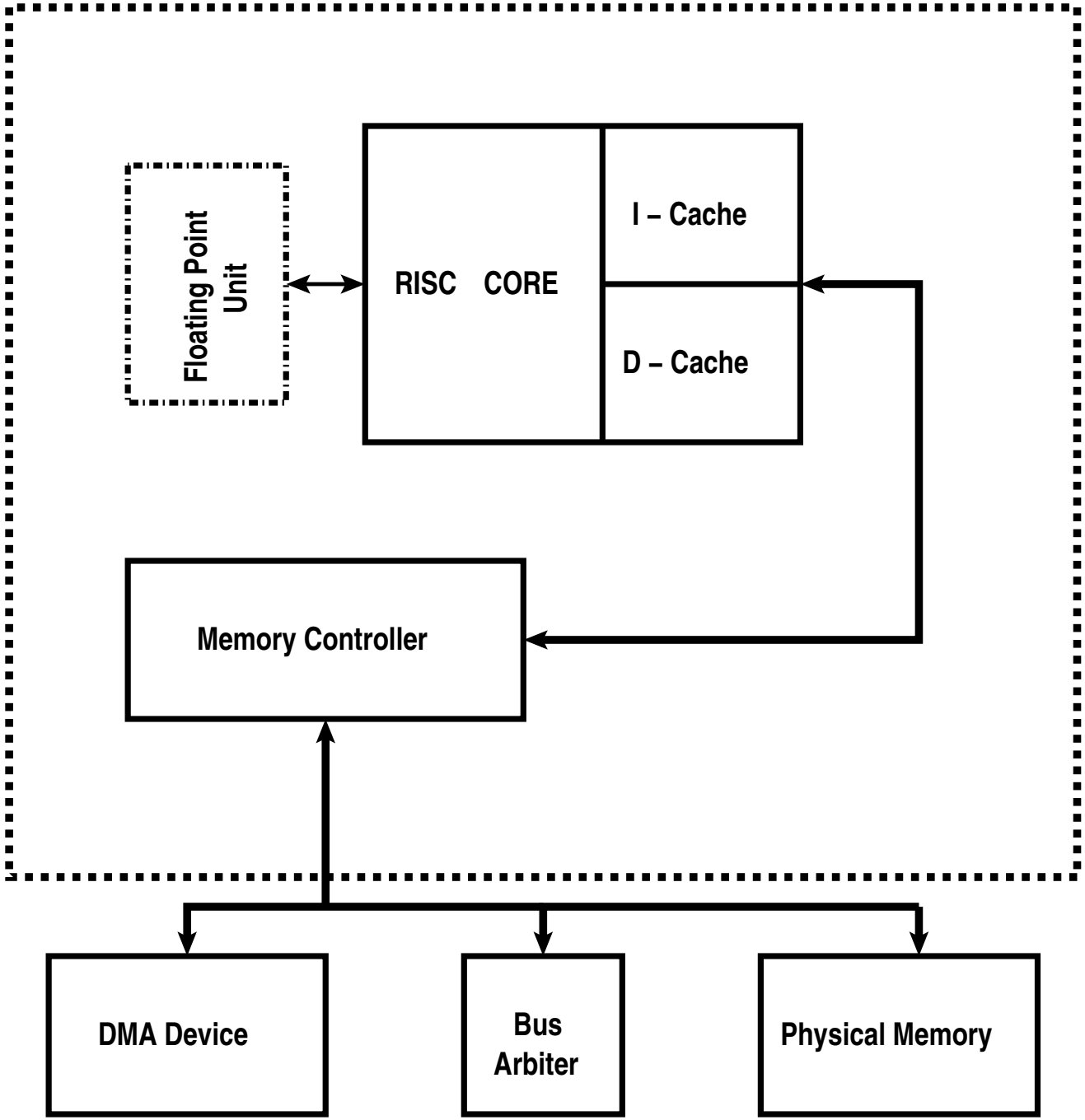
Floating Point Unit
RISC CORE
I – Cache
D – Cache
Memory Controller
DMA Device
Bus Arbiter
Physical Memory