# CSL 356: Analysis and Design of Algorithms

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- Efficient certification:
  - We say an algorithm B is an efficient certifier for a problem X if the following holds:
    - B is a polynomial time algorithm that takes two input strings s and t.
    - There is a polynomial p such that for every string s, we have that s is in X if and only if there exists a string t such that  $|t| \leq p(|s|)$  and B(s,t) = 1.
- <u>NP</u>: This is the set of all problems for which there exists an efficient certification algorithm.
- NP stands for Non-deterministic Polynomial time:
  - Non-deterministic algorithms are allowed to make nondeterministic choices (guesswork). Such algorithms can guess the certificate *t* for an instance *s*.

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- <u>P</u>: This is the set of all problems for which there exists an efficient algorithm that solves the problem. **P** stands for polynomial time.
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  - <u>Proof</u>: The certificate is an assignment. The certifier checks if this assignment makes all clauses true.

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- What are the hardest problems in **NP**?
- A problem X in NP is the hardest problem in NP if for all problems Y in NP, Y ≤<sub>p</sub> X.
- Such problems are called **NP**-complete problems.

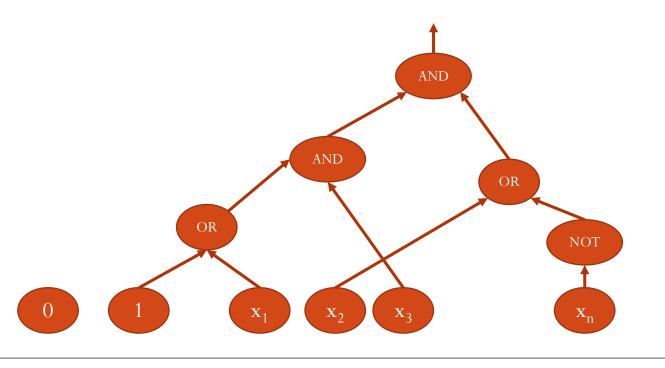
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- How do we show that there is a problem that is **NP**-complete?
- Suppose by some "magic" we have shown that SAT is NP-complete.
  Does that mean that there are more NP-complete problems?

Computational Intractability: NP & NP-complete

- <u>Theorem(Cook-Levin)</u>: 3-SAT is **NP**-complete.
  - <u>Proof idea</u>:
    - <u>Claim 1</u>: Circuit-SAT is **NP**-complete.
    - <u>Claim 2</u>: Circuit-SAT  $\leq_p$  3-SAT.

- <u>Circuit</u>: A directed acyclic graph where each node is either
  - <u>Constant nodes</u>: Labeled 0/1
  - <u>Input nodes</u>: These denote the variables.
  - <u>Gates</u>: AND, OR, and NOT. There is a single output node.



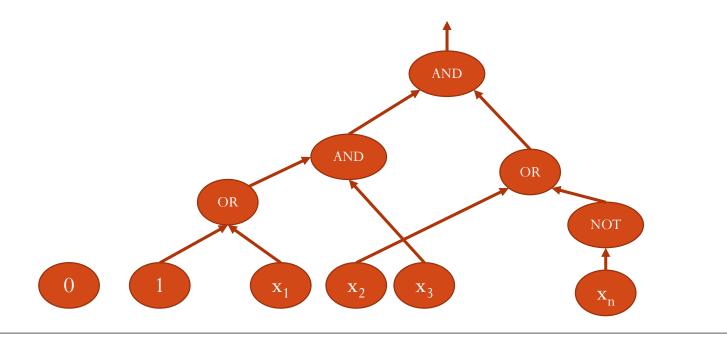
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- <u>Problem(Circuit-SAT)</u>: Given a circuit determine if there is an input such that the output of the circuit is 1.
- <u>Claim 1</u>: Circuit-SAT is **NP**-complete.
- <u>Fact</u>: For every algorithm that runs in time polynomial in the inputs size *n*, there is a circuit of size polynomial in *n* such that the output of both are the same.

- <u>Problem(Circuit-satisfiability)</u>: Given a circuit determine if there is an input such that the output of the circuit is 1.
- <u>Claim 1</u>: Circuit-SAT is **NP**-complete.
- <u>Fact</u>: For every algorithm that runs in time polynomial in the inputs size n, there is an equivalent circuit of size polynomial in n.
- <u>Proof of Claim 1</u>: ?

- <u>Problem(Circuit-satisfiability)</u>: Given a circuit determine if there is an input such that the output of the circuit is 1.
- <u>Claim 1</u>: Circuit-SAT is **NP**-complete.
- <u>Fact</u>: For every algorithm that runs in time polynomial in the inputs size *n*, there is an equivalent circuit of size polynomial in *n*.
- <u>Proof of Claim 1</u>: Given an input instance *S* of any NP problem *X*, consider the equivalent circuit for the efficient certifier of *X*. The input gates of this circuit has *S* and *t*.
  - <u>Claim 1.1</u>: If S is in X, the circuit is satisfiable.
  - <u>Claim 1.2</u>: If S is not in X, then the circuit is not satisfiable.

- <u>Problem(Circuit-satisfiability)</u>: Given a circuit determine if there is an input such that the output of the circuit is 1.
- <u>Claim 1</u>: Circuit-SAT is **NP**-complete.
- <u>Claim 2</u>: Circuit-SAT  $\leq_p$  3-SAT.
  - <u>Proof</u>: For any circuit, we can write an equivalent 3-SAT formula.



## **Computational Intractability**

NP and NP-completeness

Computational Intractability: NP & NP-complete

- <u>NP</u>: A problem X is in **NP** if and only if there is an *efficient certifier* for X.
- <u>NP-complete</u>: These are all problems *X* with the following properties:
  - *1. X* is in **NP**.
  - 2. For all Y in NP,  $Y \leq_p X$ .
- <u>Theorem (Cook-Levin)</u>: 3-SAT is **NP**-complete.
- <u>NP-hard</u>: These are all problems *X* with the following property:
  - 1. For all Y in NP,  $Y \leq_p X$ .
  - <u>Example</u>: Given a graph G, find the maximum independent set in G.

## **Computational Intractability**

NP-complete problems

- We now know that 3-SAT is **NP**-complete.
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- <u>Claim</u>: Independent-set, Vertex-cover, Set-cover are also NPcomplete.
  - <u>Proof</u>: These problem are in **NP** and
    - 3-SAT  $\leq_p$  Independent-set  $\leq_p$  Vertex-cover  $\leq_p$  Set-cover.

- <u>Problem (TSP)</u>: Given a *complete*, weighted, directed graph G and a number k, determine if there is a tour in the graph of total length at most k.
- <u>Claim 1</u>: TSP is in **NP**.
  - <u>Proof</u>: A tour of length at most k is a certificate.

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- <u>Claim 1</u>: TSP is in **NP**.
  - <u>Proof</u>: A tour of length at most k is a certificate.
- <u>Claim 2</u>:  $3\text{-SAT} \leq_p \text{TSP}$ 
  - <u>Proof</u>:
    - <u>Claim 2.1</u>: 3-SAT  $\leq_p$  Hamiltonian-cycle.
    - <u>Claim 2.2</u>: Hamiltonian-cycle  $\leq_p$  TSP.

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  - <u>Proof</u>:
    - <u>Claim 2.1</u>: 3-SAT  $\leq_p$  Hamiltonian-cycle.
    - <u>Claim 2.2</u>: Hamiltonian-cycle  $\leq_p$  TSP.
- <u>Problem (Hamiltonian-cycle)</u>: Given an unweighted, directed graph, determine if there is a Hamiltonian cycle in the graph.
  - <u>Hamiltonian cycle</u>: A cycle that visits each vertex exactly once.

- <u>Claim 2.2</u>: Hamiltonian-cycle  $\leq_p$  TSP.
  - <u>Proof</u>: Given a unweighted, directed graph G, construct the following complete, directed, weighted graph G:
    - For each edge (u, v) in G give the weight of 1 to edge (u, v) in G'.
    - For each pair (u, v) such that there is no edge from u to v in G, add an edge (u, v) with weight 2 in G'.
    - <u>Claim2.2.1</u>: *G* has a Hamiltonian cycle if and only if *G*' has a tour of size at most *n*.

## End